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Facsimile Number: 571-273-8300	Transmission Date: August 3, 2007

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patentee:	Tseng	Docket No.:	TSM03-0763
Serial No:	10/828,884	Patent No.:	7,223,667 B2
Date Filed:	April 21, 2004	Issue Date:	May 29, 2007
Title:	Compensated Linearity Voltage-Control-Capacitor Device by Standard CMOS Process		

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	Application Number	10/828,684
	Filing Date	April 21, 2004
	First Named Inventor	Tseng
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